

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1516980
<b>Application Number:</b>	09915437
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7370
<b>Title of Invention:</b>	Method of logic circuit synthesis and design using a dynamic circuit library
<b>First Named Inventor/Applicant Name:</b>	Sang Hoo Dhong
<b>Customer Number:</b>	35236
<b>Filer:</b>	Russell Duane Culbertson/Gracie Solis
<b>Filer Authorized By:</b>	Russell Duane Culbertson
<b>Attorney Docket Number:</b>	AUS9-2001-0301US1
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<b>Application Type:</b>	Utility

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Response to Election / Restriction Filed	25_1061_Response070126 RR_asfiled.pdf	90766	no	2

### Warnings:

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>90766</td></tr></table>	Total Files Size (in bytes):	90766
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